

CACHE ARCHITECTURE TO REDUCE LEAKAGE POWER CONSUMPTION

Abstract of the Disclosure

By dividing a cache into a core and a level 2 cache,  
faster components may be used in the core and slower  
5 components may be used in the level 2 cache. Functions  
that do not need to use the faster components, including  
the logic to manage the core, may be placed in a level 2  
cache. As a result, the core may be of reduced size and  
multilevel cache with reduced leakage current may result in  
10 some embodiments.